

01-0702

A

01/04/02
jc812 U.S. PTO

jc872 U.S. PTO
10/037251
01/04/02

EXPRESS MAIL LABEL NO. ET183432287US

Date of Deposit 1/4/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Appn. Ser. No Not Yet Ass'd : Art Unit Not Yet Ass'd
Filed Herewith : Examiner Not Yet Ass'd
Inventor Hougham et al. : Atty. Dkt. No. YOR920010020US1

For: MULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

PATENT APPLICATION TRANSMITTAL LETTER

Assistant Director for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is a patent application including:

This Transmittal Letter - 1 page

Specification - 15 pages

Informal Drawings - 3 sheets

Declaration - 4 pages

✓ Assignment + cover - 3 pages

Information Disclosure Transmittal with references, and,

Deposit Acct. Authorization for Filing Fee and As'mt recordation fee.

The filing fee has been calculated as shown below:

	Basic Fee	\$740.00
Total claims 15 - 20 = 0 extra		00.00
Independent claims 3 - 3 = 0 extra		00.00
Filing fee	Total	\$740.00

The assignment recordation fee of \$40.00

A deposit account authorization for \$ 780.00 is included

Respectfully submitted,

by

Alvin J. Riddles 1/4/02

Alvin J. Riddles

Reg. No. 17862

Dkt. No. YOR920010020US1

EXPRESS MAIL LABEL NO. ET183432287US

Date of Deposit 1/4/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Appn. Ser. No Not Yet Ass'd : Art Unit Not Yet Ass'd

Filed Herewith : Examiner Not Yet Ass'd

Inventor Hougham et al. : Atty. Dkt. No. YOR920010020US1

For: MULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

DEPOSIT ACCOUNT CHARGE AUTHORIZATION LETTER

Assistant Director for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231
Sir:

For the Filing Fee and the Assignment recordation Fee for the above identified application a charge authorization of \$ 780.00 on Deposit Account 50-0510 is provided herewith.

Respectfully submitted,

by *Alvin J. Riddles* 1/4/02
Alvin J. Riddles
Reg.No. 17862

Dkt. No. YOR920010020US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Appn. Ser. No Not Yet Ass'd : Art Unit Not Yet Ass'd
Filed Herewith : Examiner Not Yet Ass'd
Inventor Hougham et al. : Atty. Dkt. No. YOR920010020US1
For: MULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

EXPRESS MAIL CERTIFICATE

Assistant Director for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231
Sir:

EXPRESS MAIL LABEL NO. ET183432287US

Date of Deposit 1/4/02

I hereby certify that the following attached paper or fee

Patent Application including

Transmittal Letter – 1 page

Specification - 15 pages

Informal Drawings – 3 sheets

Declaration – 4 pages

Assignment + cover–3 pages

Information Disclosure Transmittal with references, and,

Deposit Acct. Authorization for Filing Fee and As'mt recordation fee.

is being deposited with the United States Postal Service “Express Mail Post Office to Addressee” service under 37CFR 1.10 on the date indicated above and is addressed to

Assistant Director for Patents

U.S. Patent and Trademark Office

Washington, D.C. 20231

Alvin J. Riddles

Typed or Printed Name of person mailing paper or fee

Alvin J. Riddles 1/4/02
Signature and date of person mailing paper or fee

FIG 2

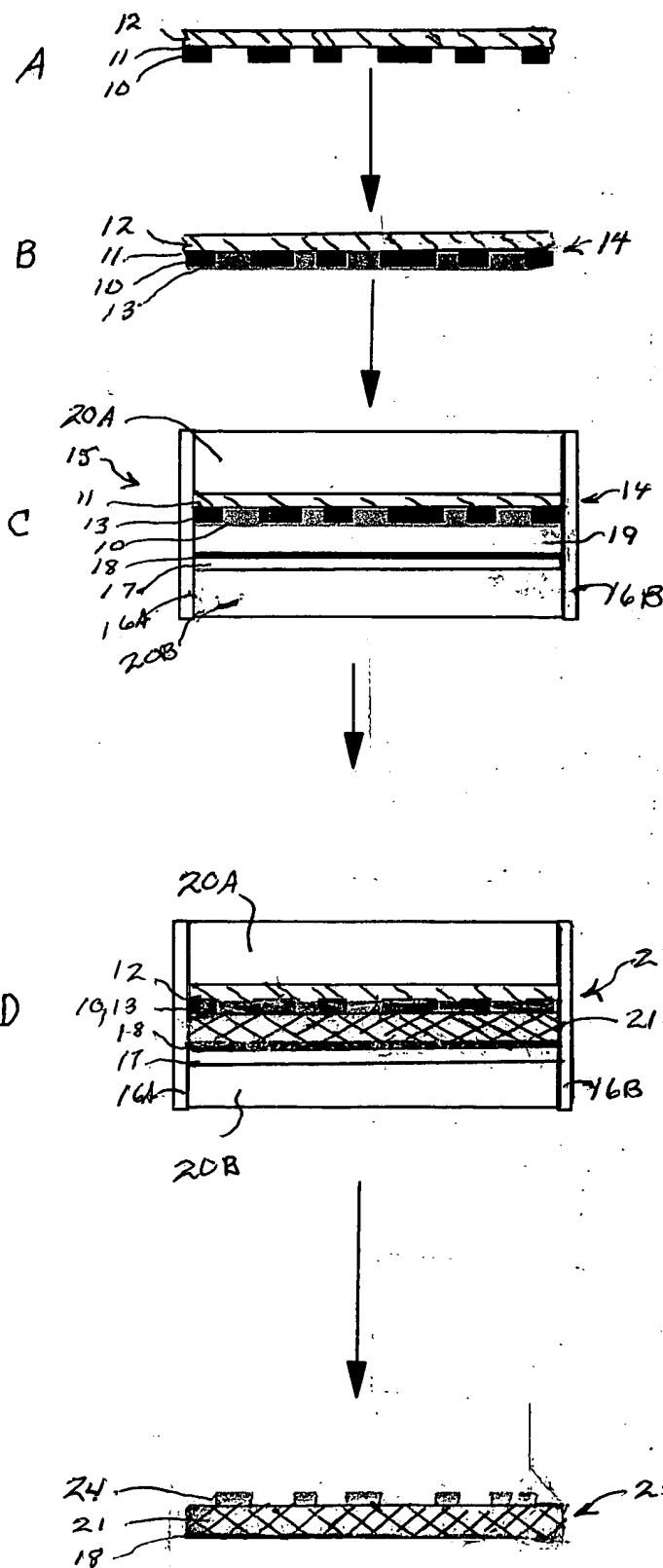


FIG. 2

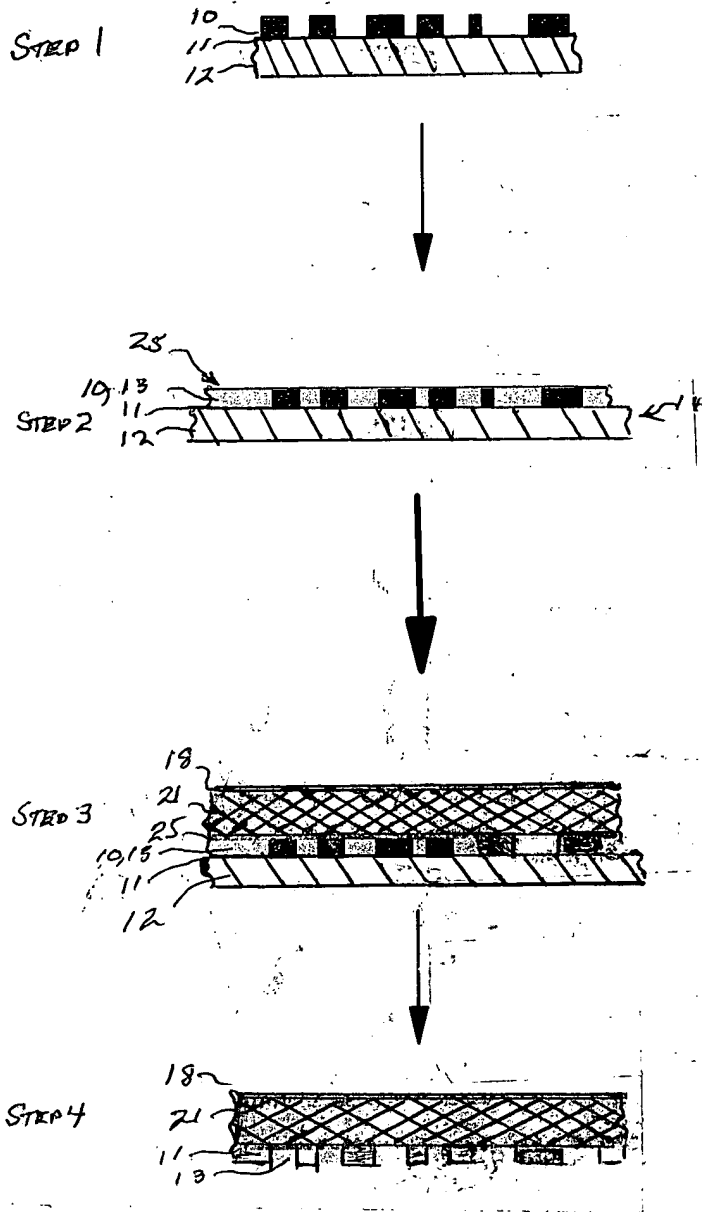
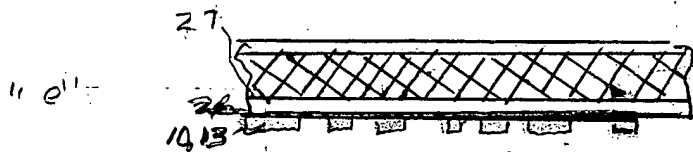
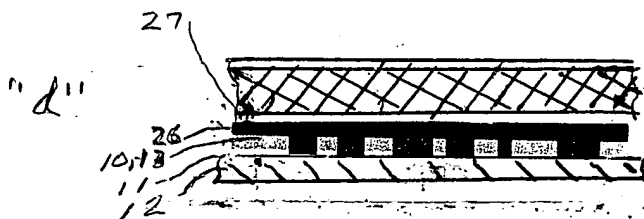
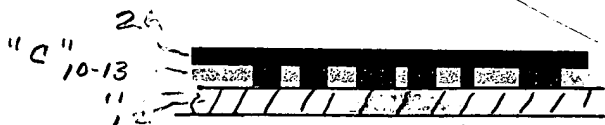
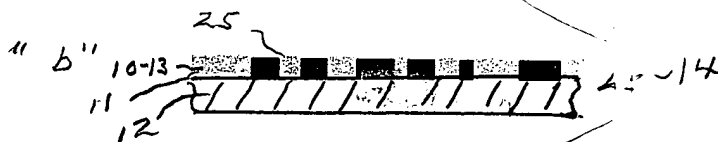


FIG 3



2025 RELEASE UNDER E.O. 14176

MULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

Field of the invention

The invention is in the field of microcontact printing in the fabrication of electronic circuitry wherein a stamp member having some resilience is employed to transfer a pattern of an etching responsive material onto a substrate surface and in particular to the use of separate layers in the printing stamp structure for imparting particular properties to different parts of the stamp such as surface and bulk.

Background of the invention

The technique of microcontact printing has been referred to in the art as soft lithography and may be considered as being an ultrafine resilient stamping process wherein monolayers of etchant resistant materials or seed catalysis materials are transferred onto substrate surfaces with precision and resolution.

The present state of the art is considered represented by such technical articles as: Kumer et al, App. Phys. Ltrs., 63,(14), 4Oct93, P2002-2004, titled "Features of gold having micrometer to centimeter dimensions can be formed through a combination of stamping with an elastomeric stamp and an alkanethiol "ink" followed by chemical etching"; wherein there is reported the use of an elastomeric stamp to transfer fine patterns of gold using an alkanethiol ink: and Hidber et al, Langmuir, 1996, 12, Pages 1375 - 1380, titled " Microcontact Printing of Palladium Colloids: Micron-Scale Patterning by Electroless Deposition of Copper"; wherein there is reported the

deposition of colloids that serve as catalysts for the electroless deposition of copper.

As progress advances , needs are developing for providing a tuned surface character of the stamp member that will effectively pick up the ink to be transferred and then deposit it when the stamp member has been brought into contact with the substrate; for providing a tuned elastic modulus for reproducible resilience in the stamp member; and for providing specific stamp capabilities. Heretofore in the art the stamp members have been made of a single material, plus at times a metal backplane support and tradeoffs have been required between surface and bulk properties of the stamp material.

Summary of the invention

The invention teaches a resilient microcontact printing stamp technology involving constructing the stamp of layers having assigned properties. For example separation of the functions of the surface characteristics and the bulk characteristics of the stamp material are achieved by providing a layered structure wherein the layer at the surface has carefully chosen surface properties and is positioned on an underlying layer with carefully chosen bulk mechanical properties, and other unique functional properties can be imparted through an intermediate layer. A fabrication process is provided that employs a coating capability for one portion, an injection capability for another property and a porosity property for still another portion.

Brief description of the drawings

Figure 1 is an illustration of the primary considerations through depictions of the

intermediate structures occurring in the fabrication of the multilayer printing stamp of the invention.

Figure 2 is an illustration of the steps involved in the multilayer printing stamp fabrication wherein a mold housing is eliminated from the figures to provide illustrative clarity.

Figure 3 is an illustration of the extension of the principles of the invention to permit the providing of a special function layer.

Description of the invention

In the fabrication of very finely patterned resilient stamp members that are to be used for such purposes as the printing of seed layers of metal for the plating of patterns for electronic circuitry, needs are being encountered where multiple material properties not usually found in a single material would be beneficial. For example the surface of the stamp would have wettability properties optimized for wetting the liquid to be used as the seed material in the plating, while at the same time the stamp must be made structurally durable with appropriate stiffness that maintains integrity of the pattern; and further such properties as porosity in a specific location is useful for fluid reservoir capability. Frequently in the art situations arise where in a material optimization of one property operates to compromise another.

In accordance with the invention the material property consideration can be overcome by providing stamps with layers each having a desired individual property such as would be the situation with a bilayer stamp. In such a situation a very thin region comprising the raised relief

patterned features of the stamp is made in one material optimized for a certain set of properties, while the bulk of the stamp can be made of a second material with other properties. The materials with different properties can conveniently be fabricated as layers applied as separate steps.

As an illustration of the primary considerations involved in the fabrication of the bilayer microcontact printing stamp of the invention a flow chart type depiction is provided in Figure 1 of the intermediate structures in the assembly of a surface layer, a bulk layer and a backplane.

Referring to Figure 1 the flow chart has items A through E which are depictions of intermediate structures produced in the fabrication of the stamp of the invention. At item A a mold master pattern structure is produced in which a relief pattern 10 of the ink transfer pattern of the stamp to be produced, hereinafter referred to as the pattern, is formed on a surface 11 of a supporting substrate 12. The substrate 12 has the properties of imparting stiffness, flatness and permitting adherence by the pattern. The material glass is generally satisfactory.

The pattern 10 is formed by standard lithographic techniques on the surface 11 in a negative relief, in which in other words, indicates that the spaces between the pattern features are to be the raised relief portions of the final stamp once made. On the surface of the pattern 10 a relatively thin layer 13 of the material that is to become the surface of the stamp is applied. The layer 13 has properties such:

that it can be applied in a thin layer such as by the technique of spinning,

that in the uncured state it enters the interstices between the embossed portions

of the pattern 10,

that it can be made relatively handleable by a partial curing operation involving a mild amplitude and duration heat cycle, such that mixing does not take place with a subsequently added layer.

The material siloxane is one example of an appropriate material for layer 13. Commercially available silane material is the material known as Dow Corning Sylgard 184.

At item B the structure is given the reference designation 14. It now has the pattern 10 on the surface 11 of the substrate 12, with the interstices filled with the material of the layer 13, any excess having been removed so that the surface is made up of embossed elements of the pattern 10 and interstice elements of the material 13, hereinafter referred to as 10-13, and with the structure 14 having been subjected to a partial curing operation, so that it may now be handled for further processing.

That further processing involves, as illustrated in item C, the positioning of the structure 14 in a mold type apparatus for an injection operation. In item C the structure 14 is positioned in the mold 15 having sides such as 16A and 16B arranged such that the structure 14 is supported and surrounded.

Further in the illustration in item C, a supporting plate 17, of for example glass, is positioned in the bottom opening of the mold 15 and a relatively thin layer 18 for example of a flexible sheet metal material that will serve as a bottom surface of the being constructed stamp is placed over the plate 17. The relative positioning provides an internal gap 19 in the mold 15 between the to be surface layer 18 and the 10 - 13 face of the structure 14. The mold member 15

has top 20 A and bottom 20 B supporting inserts. There is the capability, not shown, for injecting material into and filling the gap 19 of the structure of item C .

Referring to item D, the gap 19 of item C is filled with a precursor mix of a bulk producing material 21 that will, on curing such as with appropriate temperature cycle, impart the bulk structural properties of the being constructed stamp and cause the optimized adhesion properties of the material 13 to adhere to the bulk material 21. A satisfactory material for the precursor mix is a fluid solution of the material siloxane of which the material known as DowCorning Sylgard 186 is an example. Where the material 13 is only partially cured at the intermediate structure 14 stage, a cross reaction occurs at the interface and a superior adhesion to the material 21 in the structure in item D is achieved.

Upon curing, the structure labelled 22 is in the mold 15 ready for removal of the top 20A, bottom 20 B and sides labelled 16A and 16 B as in the illustration item D. The intermediate structure 22 includes the glass substrate 12 layer, the interstices filled pattern 10,13 layer, the cured bulk layer 21, the surface layer 18 and the glass plate 17.

The finished stamp 23 is illustrated in item E.

After removal of structure 22 from the mold, the glass layer 17 is removed leaving exposed the surface layer 18 on one face, and on the other face, the glass layer 12 is removed along with the master pattern 10. An operation either simultaneously with removal of the glass layer 17 or etching is conducted at the 10-13 surface, removing the embossed portions of the master 10 and

exposing a positive relief siloxane element pattern 24, each element of which is adhering the optimized adhesion properties to the bulk siloxane body 23.

In Figure 2 an illustration is provided of the intermediate structures produced in the multilayer printing stamp fabrication of the invention wherein a mold housing is eliminated from the figures to provide illustrative clarity for the specific functional steps. The same reference numerals as in Fig. 1 as appropriate are used.

Referring to Figure 2 the flow chart has steps 1 to 4 which are depictions of intermediate structures produced in the fabrication of the stamp of the invention. At step 1, the negative relief pattern 10 of the ink transfer pattern of the stamp to be produced, hereinafter referred to as the pattern, is formed on a surface 11 of a supporting, glass for example, substrate 12. The material of the pattern 10 may have properties that are optimized for wettability in operations after the stamp has been produced. The substrate 12 has the properties of imparting stiffness, flatness and permitting adherence by the pattern 10. The pattern 10 is a negative relief which is that the spaces between the pattern features are to be the positive embossed portions of the final stamp. The pattern may be formed by standard lithographic techniques on the surface 11 of the substrate 12.

In step 2, over the surface of the pattern 10 a relatively thin quantity of material 13 that is to become the surface of the stamp, is applied in an uncured state such as by spinning and possibly followed by squeegeeing to remove excess and to force the material 13 into the interstices between the embossed portions of the pattern 10 so as to form the layer labelled 10 - 13. The structure is then made relatively handleable by a partial curing operation involving a mild

amplitude and duration heat cycle, such that mixing does not take place with a subsequently added layer. The structure is labelled 14 and the surface of the 10-13 layer is labelled 25.

In step 3 the material that is to be the bulk of the stamp is applied on the surface 25.

Referring to step 3, a quantity of a material that will provide the bulk stiffness of the completed stamp is applied to the surface 25 as a relatively thick layer corresponding to previously discussed layer 21 using a technique such as injection molding. The layer 21 is then covered with a thin layer for handling and overall shape retention, labelled 18.

The completed stamp is produced through step 4.

Referring to step 4 the stamp is removed from the substrate 12 by peeling along the surface 11 leaving exposed the layer 10-13. The layer 10-13 may be subjected to an etching operation that removes the negative elements of the pattern 10 and leaves remaining the positive relief of elements of 13 that occupied the interstices of the pattern; or the pattern 10 sticks to the glass of surface 11 and is simultaneously peeled away from the stamp.

The principles of the invention can be extended to permit the providing of a special function layer in the stamp.

Figure 3 is an illustration of the extension of the principles of the invention to multi layer structures such as the providing of an intermediate layer to act as an ink reservoir or serve as a gradient layer to change between extreme differences in properties from that required for the surface and the bulk.

Referring to Figure 3 a procedure is illustrated similar to the procedure of Figure 2 and including an added layer capability. The same reference numerals as in previous figures are used where appropriate.

In Figure 3 the procedure has stages "a" to "e" which are depictions of intermediate structures produced in fabrication. At "a" the negative relief pattern 10 is formed on a surface 11 of a supporting, glass for example, substrate 12. The material of the pattern 10 may have properties that are optimized for wettability in operations after the stamp has been produced. The substrate 12 has the properties of imparting stiffness and flatness and permitting adherence by the pattern 10. The pattern 10 is a negative relief which is that the spaces between the pattern features are to be the positive relief embossed portions of the final stamp. The pattern may be formed by standard lithographic techniques on the surface 11 of the substrate 12.

At "b", over the surface of the pattern 10 a relatively thin quantity of material 13 that is to become the surface of the stamp in an uncured state, is applied such as by spinning followed by squeegeeing to remove excess and to force the material 13 into the interstices between the embossed portions of the pattern 10 so as to form the layer labelled 10 - 13. The structure is then made relatively handleable by a partial curing operation involving a mild amplitude and duration heat cycle, such that mixing does not take place with a subsequently added layer. The structure is labelled 14 and the surface of the 10-13 layer is labelled 25.

At "c", over the surface 25 a relatively thin quantity of a material 26 that is to have a selected property such as for example porosity which property permits serving as an ink

reservoir in the stamp.

At "d" over the surface of the material 26 a relatively thick layer of a material capable of providing bulk properties is applied such as by injection as done in connection with Figure 2.

The completed stamp is at "e".

Referring to "e" the stamp is removed from the substrate 12 by peeling along the surface 11 leaving exposed the layer 10-13. The layer 10-13 is then subjected to an etching operation if necessary that removes the negative elements of the pattern 10 and leaves only remaining the positive relief of elements of 13 that occupied the interstices of the pattern and that will carry the transfer ink of the stamp.

What has been described is a technique of stamp fabrication wherein multiple layers are employed and each provides an independent property.

What is claimed is:

- 1 1. In microcontact printing wherein an electronic circuitry pattern on the surface of an
2 elastomeric stamp member is operable in a transfer of a further processing responsive
3 material, to a surface of a substrate,
4 the improvement comprising:
5 said elastomeric stamp member having a surface region of a material imparting to said
6 stamp member at least one of the properties of adhesion and wettability enhancement
7 of the material of said circuitry pattern to said surface region, and,
8 said elastomeric stamp member further having at least one subsurface region, each said
9 subsurface region being of a material imparting a particular physical property to said
10 stamp member.
- 1 2. The microcontact improvement of claim 1 wherein said at least one subsurface
2 region, is a single region that imparts the bulk property of stiffness to said stamp
3 member.
- 1 3. The microcontact improvement of claim 1 wherein said at least one subsurface
2 region, is a single region that imparts the bulk property of wettability enhancement to said
3 stamp member.
- 1 4. The microcontact printing improvement of claim 2 wherein another region of said at
2 least one subsurface regions, imparts the property of porosity, and is positioned between
3 said surface region and said stiffness bulk property imparting region.

1 5. The microcontact printing improvement of claim 2 wherein another region of said at
2 least one subsurface regions, imparts the property of porosity, and is positioned between
3 said surface region and said wettability enhancement bulk property imparting region.

1 6 .The microcontact printing improvement of claim 2 wherein said surface region is of the
2 material known as Dupont Sylgard siloxane 184 and said subsurface region is of the
3 material known as Dupont Sylgard siloxane 186.

1 7. A microcontact printing stamp,
2 comprising in combination :
3 a body having at least a layer imparting a bulk stiffness and flatness physical property on
4 which there is a stamping pattern supporting surface,
5 a stamping pattern layer positioned on said pattern supporting surface of said body,
6 said stamping pattern layer including a negative relief stamping pattern in which the
7 spaces between the features of said stamping pattern are the positive relief embossed
8 portions of the final printing stamp,
9 said stamping pattern layer further being of an electronic circuitry processable material
10 in which at least one of the physical properties of adhesion enhancement and
11 wettability enhancement are imparted.

1 8. The microcontact printing stamp member of claim 7 including a further layer
2 of a specific physical property imparting material positioned between said stamping
3 pattern layer and said layer of bulk stiffness and wettability enhancement physical

4 property imparting material.

1 9. The microcontact printing stamp member of claim 8 wherein said physical property
2 imparted by said layer of a specific physical property imparting material is the physical
3 property of porosity.

1 10. The microcontact printing stamp of claim 7 wherein said layer of a bulk stiffness and
2 wettability enhancement physical property imparting material, is the material known as
3 Dupont Sylgard siloxane 186 and the material of said stamping pattern layer is of the
4 material known as Dupont Sylgard siloxane 184.

1 11. The fabrication process of a microcontact printing stamp member ,
2 comprising in combination the steps of :
3 providing a conductor pattern member, having first and second parallel surfaces,
4 said pattern member material between said first and second surfaces, being
5 comprised of a negative relief of a conductor correlatable
6 configuration of elements of a material processable into electronic circuitry,
7 said negative relief of a conductor correlatable configuration of elements having
8 an adhesion enhancing material interspersed between conductor portions,
8 positioning said first surface of said pattern member on a processing substrate
9 providing a body portion including at least a layer of a stiffness enhancing material on said
10 second surface of said pattern member,
11 separating said conductor pattern member from said processing substrate along the
12 interface of said first surface of said pattern member and said processing substrate,

13 etching out the negative relief portions out of said pattern member leaving a positive relief
14 of said conductor correlatable configuration.

1 12. The process of claim 11 wherein said step of providing a body portion is performed by
2 an injection operation in a mold.

1 13. The process of claim 12 wherein said adhesion enhancing material is the
2 material known as Dupont Sylgard siloxane 184.

1 14. The process of claim 13 wherein said layer of stiffness enhancing material is the
2 material known as Dupont Sylgard siloxane 186.

1 15. The process of claim 14 including a partial curing step of the adhesion enhancing and
2 the stiffness enhancing materials prior to the separation step.

MULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

ABSTRACT

A layered structure and process for a microcontact printing stamp has individual layers chosen to impart particular properties such as one layer chosen for surface properties and another layer chosen for bulk mechanical properties. The invention is fabricated through having a first layer with coatable properties and a subsequent layer with injectable properties resulting in a layered structure wherein the layer at the surface has optimized surface properties and is positioned on an underlying layer with carefully chosen bulk mechanical properties, and other unique functional properties can be imparted through an intermediate layer. A fabrication process is provided that employs a coating capability for one portion, an injection capability for another property and a porosity property for still another portion.

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100

**DECLARATION AND POWER OF ATTORNEY FOR
PATENT APPLICATION**

As a below named inventor of this patent application titled,

M ULTILAYER ARCHITECTURE FOR MICROCONTACT PRINTING STAMPS

I hereby declare:

That my residence, county, post office address and citizenship are as stated below next to my name and signature;

That I am an original first and joint inventor of the subject matter which is claimed in the above identified patent application;

I hereby acknowledge the duty to disclose information which is material to the patentability of the above identified patent application in accordance with Title 37 Code of Federal Regulations, Section 1.56;

I hereby state that I have reviewed and understand the contents of the above identified patent application, including the claims;

I hereby state that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment , or both , under section 1001 of Title 18 of the United States Code and that willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION AND POWER OF ATTORNEY FOR
PATENT APPLICATION

Page 2 of 4

IBM Docket No. YOR920010020US1

I hereby state that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code and that willful false statements may jeopardize the validity of the application or any patent issued thereon.

That as a named inventor I hereby appoint the following attorney(s) and /or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Manny W. Schecter (Reg.No.31,722), Lauren C. Bruzzone (Reg.No. 35,082), Christopher A. Hughes (Reg.No. 26,914), John E. Hoel (Reg. No.26,279), Joseph C. Redmond (Reg. No. 18,753), Stephen C. Kaufman (Reg. No. 29,551), Robert M. Trepp (Reg. No. 25,933), Louis P. Herzberg (Reg. No. 41,500), Daniel P. Morris (Reg.No. 32,053), Paul J. Otterstedt (Reg. No. 37,411), Louis J. Percello (Reg. No. 33,206), Douglas W. Cameron (Reg. No. 31,596), Marian Underweiser (Reg.No. 46,134), Richard M. Ludwin (Reg. No. 33,010), Marc A. Erlich (Reg. No. 39,966), Robert P. Tassinari (Reg. No. 36,030), Derek S. Jennings (Reg.No. 41,473), Gail Zarick (Reg. No. 43,303) and Timothy M. Farrell (Reg. No. 37,321).

Send correspondence to Alvin J. Riddles

Box 34, Candlewood Isle,
New Fairfield, Ct. 06812

Direct Telephone calls to (914) 472-0644 (203) 746-3470

DECLARATION AND POWER OF ATTORNEY FOR
PATENT APPLICATION

Page 3 of 4

IBM Docket YOR920010020US1

Gareth Geoffrey Hougham
Full name of inventor 1

Gareth Geoffrey Hougham 12/19/01
Inventor 1- Signature and date

151 South Highland Ave, Apt. 6B, Ossining , Westchester County, New York, 10562
Inventor 1- County and residence

USA

Inventor 1 – Citizenship

Same as above

Inventor 1- Post Office Address

Mary Elizabeth Rothwell
Full name of inventor 2

Mary Elizabeth Rothwell 12/20/01
Inventor 2- Signature and date

36 Woodland Way, Ridgefield, Fairfield County, Ct. 06877
Inventor 2- County and residence

USA

Inventor 2 – Citizenship

Same as above

Inventor 2- Post Office Address

RECEIVED
FEB 20 2002
FBI NEW YORK

DECLARATION AND POWER OF ATTORNEY FOR
PATENT APPLICATION

Page4 of 4

IBM Docket YOR920010020US1

Ronald Wayne Nunes
Full name of inventor 3

Ronald Wayne Nunes 12/19/01
Inventor 3- Signature and date

9 Joann Road, Hopewell Junction, Dutchess County, New York, 12533
Inventor 3- County and residence

USA
Inventor 3 – Citizenship

Same as above
Inventor 3- Post Office Address

END OF DECLARATION